


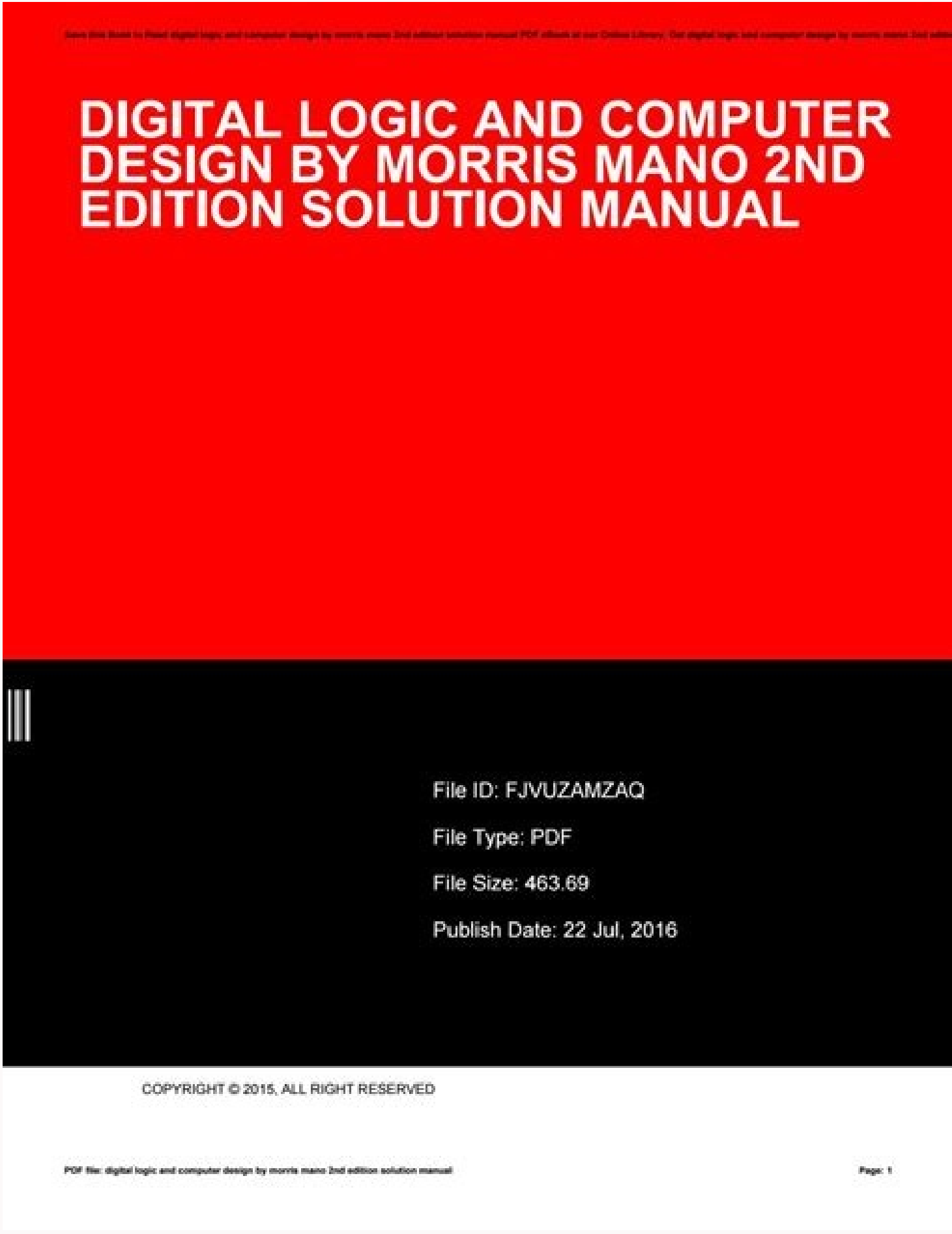
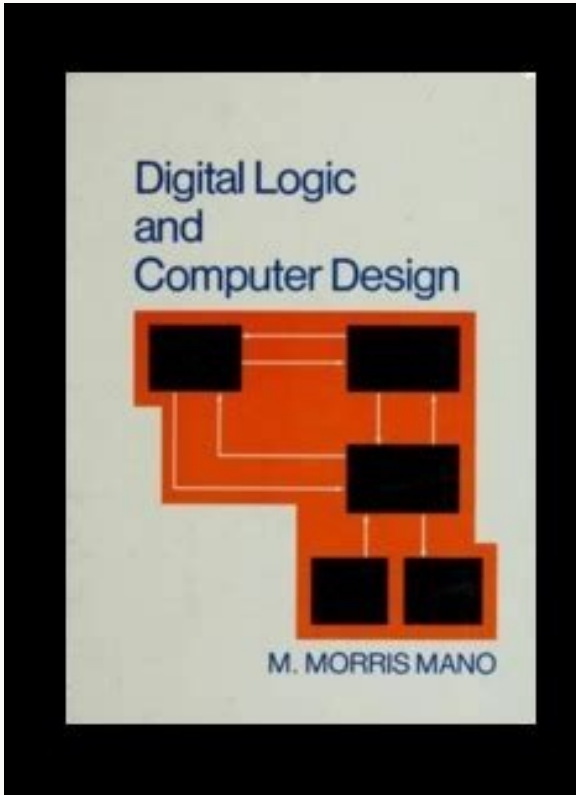
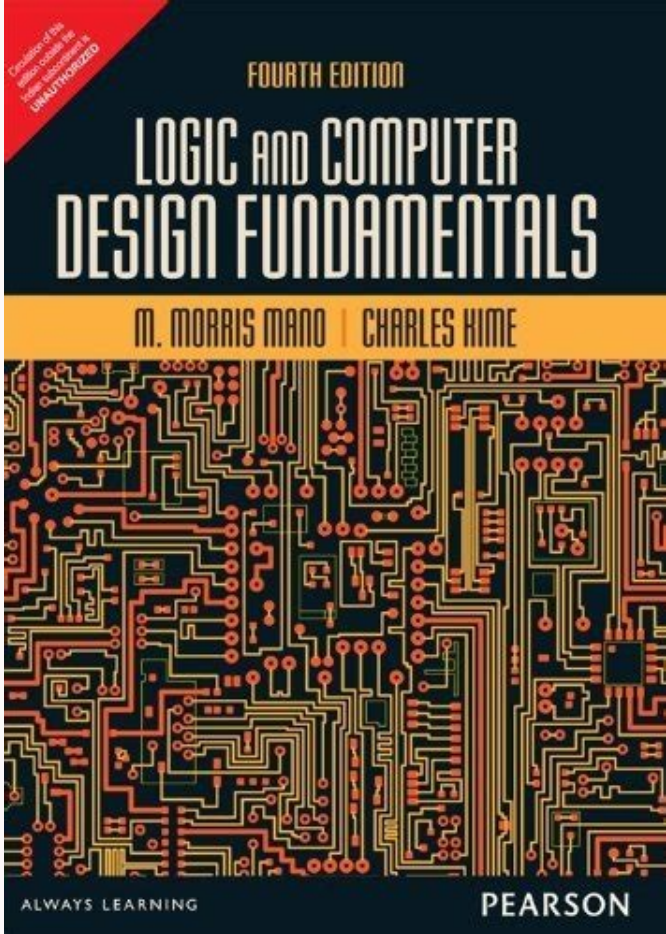
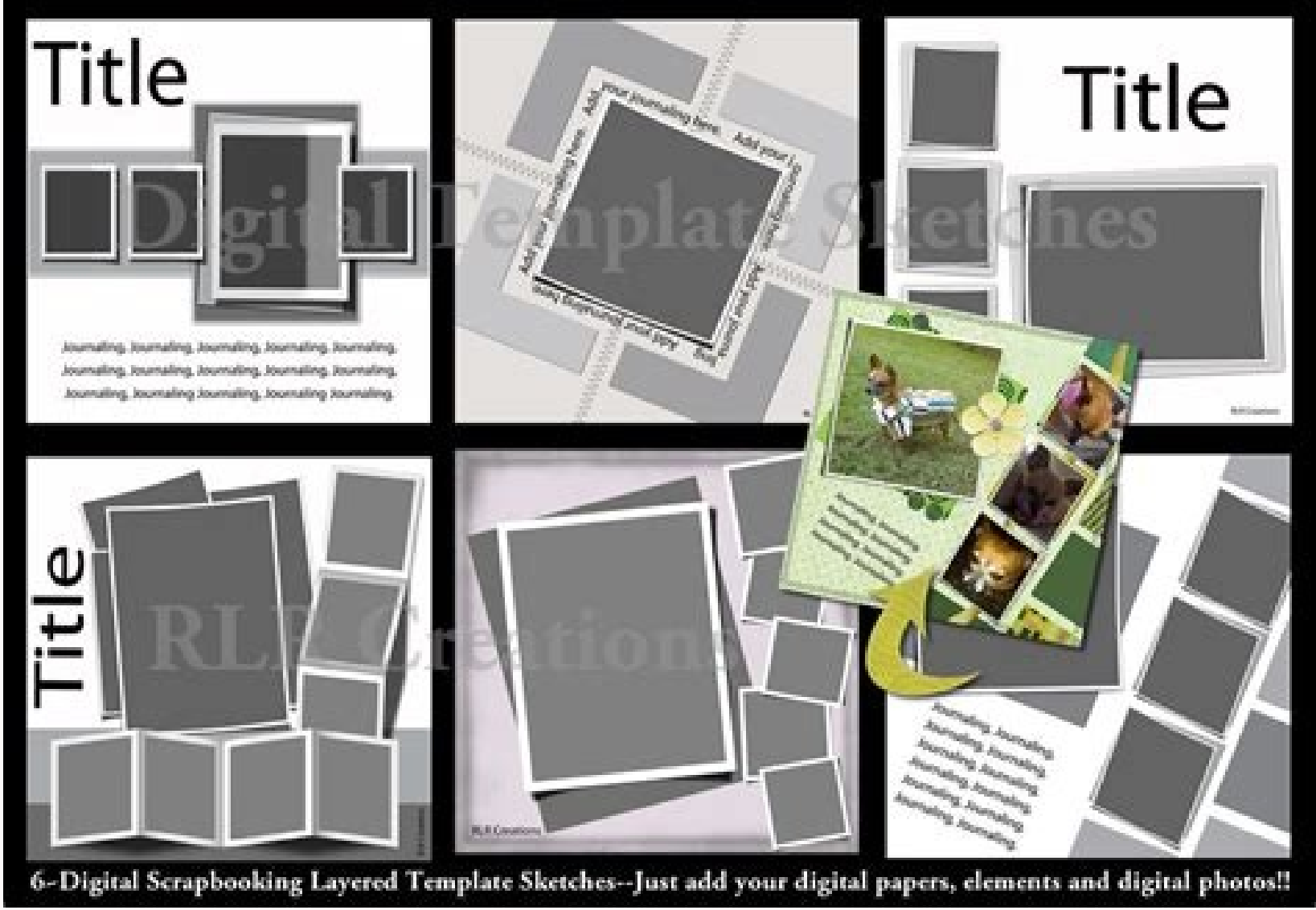
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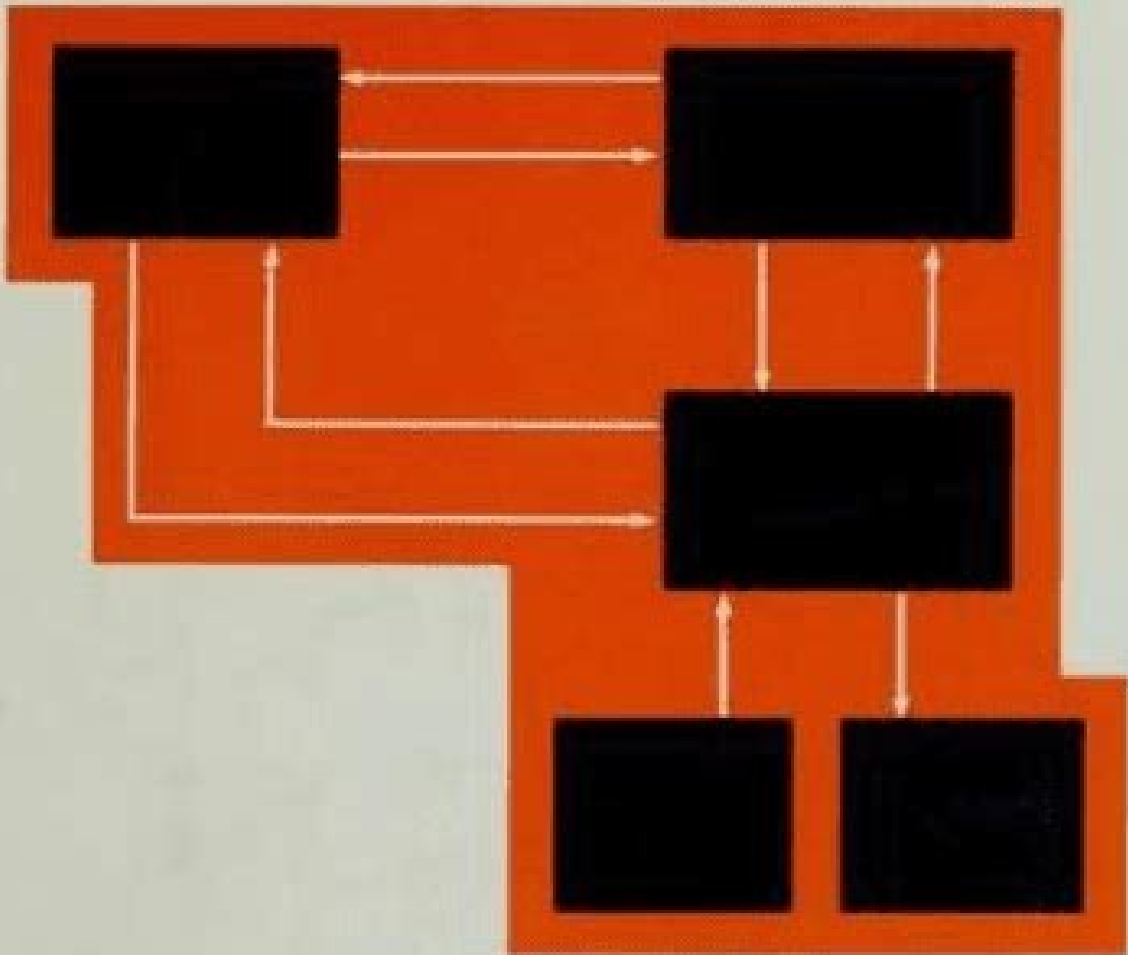
  
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# Digital Logic and Computer Design



M. MORRIS MANO

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He particularly enjoys hiking with his three sons. Mudd Professor of Engineering Design at Harvey Mudd College. When he is not working, he enjoys hiking, flying, and making things with his three sons. Associate Professor of Engineering, Harvey Mudd College, Claremont, CA, USA Sarah L. C Programming Further Reading Indo in electrical engineering and computer science from MIT. Sarah loves teaching, exploring and developing new technologies, traveling, wind surfing, rock climbing, and playing the guitar. Because our class has no prerequisites, the first half of the semester is dedicated to digital design, which is not covered by COD. Sarah has also worked with Hewlett-Packard, the San Diego Supercomputer Center, Nvidia, and Microsoft Research in Beijing. David holds about a dozen patents and is the author of three other textbooks on chip design, as well as four guidebooks to the Southern California mountains. Sarah L. You can download the paper by clicking the button above. We have undertaken to prepare such a book. We believe that building a microprocessor is a special rite of passage for engineering and computer science students. Book sale: save up to 25% on individual print and eBooks with free delivery. Microarchitecture is the link that brings it all together. This book is suitable for a rapid-paced, single-semester introduction to digital design and computer architecture or for a two-quarter or two-semester sequence giving more time to digest the material and experiment in the lab. Sequential Logic Design Chapter 4. Other universities have indicated a need for a book that combines digital design and computer architecture. Microarchitecture Chapter 8. She received her Ph.D. and M.S. in electrical engineering from Stanford University. When he is not working, he enjoys hiking, flying, and making things with his three sons. David Harris and Sarah Harris combine an engaging and humorous writing style with an updated and hands-on approach to digital design. We particularly like their coverage of the MIPS architecture and microarchitecture because MIPS is a commercially successful microprocessor architecture, yet it is simple enough to clearly explain and build in an introductory class. More details Digital Design and Computer Architecture is designed for courses that combine digital logic design with computer organization/architecture or that teach these subjects as a two-course sequence. Since then, he has consulted at Sun Microsystems, Hewlett-Packard, Evans & Sutherland, and other design companies. David's passions include teaching, building chips, and exploring the outdoors. Before attending Stanford, she received a B.S. in electrical and computer engineering from Brigham Young University. By the end of Digital Design and Computer Architecture, readers will be able to build their own microprocessor and will have a top-to-bottom understanding of how it works—even if they have no formal background in design or architecture beyond an introductory class. This book is unique in its treatment in that it presents digital logic design from the perspective of computer architecture, starting at the beginning with 1's and 0's, and leading students through the design of a MIPS microprocessor. We have used several editions of Patterson and Hennessy's Computer Organization and Design (COD) for many years at Harvey Mudd College. Featuring examples of the two most widely-used HDLs, VHDL and Verilog, the first half of the text prepares the reader for what follows in the second: the design of a MIPS Processor. Before attending Stanford, he worked at Intel as a logic and circuit designer on the Itanium and Pentium II processors. Side-by-side examples of the two most prominent Hardware Design Languages—VHDL and Verilog—illustrate and compare the ways the each can be used in the design of digital systems. MIPS Appendix C. Knovel MyiLibrary ProQuest Ebook Central Referex An electronic book accessible through the World Wide Web; click for information Safari Books Online Safari Books Online ScienceDirect ScienceDirect VLeBooks VH7QX3XE2P.search.serialsolutions.com Available from Skillsoft Books ITPro ebookcentral.proquest.com learning.oreilly.com Sarah L. David holds more than a dozen patents and is the author of three other textbooks on chip design, as well as many Southern California hiking guidebooks. From Zero to One Chapter 2. Unique presentation of digital logic design from the perspective of computer architecture using a real instruction set, MIPS. No promo code needed. Digital Design and Computer Architecture, Second Edition. Why publish yet another book on digital design and computer architecture? She received her Ph.D. and M.S. in Electrical Engineering from Stanford University. When she is not working or running after her two sons, you can find her playing music with friends, hiking, kayaking, biking, and traveling. Contents: Preface Chapter 1. He received his Ph.D. in electrical engineering from Stanford University and his M.Eng. Digital design in itself is a powerful and exciting subject. Hardware Description Languages Chapter 5. Worked examples conclude each section to enhance the reader's understanding and retention of the material. students taking a course that combines digital logic and computer architecture or students taking a two-quarter sequence in digital logic and computer organization/architecture Table of Contents 1 From Zero to One 1.1 The Game Plan 1.2 The Art of Managing Complexity 1.3 The Digital Abstraction 1.4 Number Systems 1.5 Logic Gates 1.6 Logic Levels 1.7 \* CMOS Transistors 1.8 \* Power Consumption 1.9 Summary and A Look Ahead 2 Combinational Logic Design 2.1 Introduction 2.2 Boolean Equations 2.3 Boolean Algebra 2.4 From Logic to Gates 2.5 Multilevel Combinational Logic 2.6 X's and Z's, Oh My 2.7 Karnaugh Maps 2.8 Combinational Building Blocks 2.9 Timing 2.10 Summary 3 Sequential Logic Design 3.1 Introduction 3.2 Latches and Flip-Flops 3.3 Synchronous Logic Design 3.4 Finite State Machines 3.5 Timing of Sequential Logic 3.6 Parallelism 3.7 Summary 4 Hardware Description Languages 4.1 Introduction 4.2 Combinational Logic 4.3 Structural Modeling 4.4 Sequential Logic 4.5 More Combinational Logic 4.6 Finite State Machines 4.7 \* Parameterized Modules 4.8 Testbenches 4.9 Summary 5 Digital Building Blocks 5.1 Introduction 5.2 Arithmetic Circuits 5.3 Number Systems 5.4 Sequential Building Blocks 5.5 Memory Arrays 5.6 Logic Arrays 5.7 Summary 6 Architecture 6.1 Introduction 6.2 Assembly Language 6.3 Machine Language 6.4 Programming 6.5 Addressing Modes 6.6 Lights, Camera, Action: Compiling, Assembling, and Loading 6.7 \* Odds and Ends 6.8 \* Real World Perspective: IA-32 Architecture 6.9 Summary 7 Microarchitecture 7.1 Introduction 7.2 Performance Analysis 7.3 Single-Cycle Processor 7.4 Multicycle Processor 7.5 Pipelined Processor 7.6 \* HDL Representation 7.7 \* Exceptions 7.8 \* Advanced Microarchitecture 7.9 \* Real World Perspective: IA-32 Microarchitecture 7.10 Summary 8 Memory Systems 8.1 Introduction 8.2 Memory System Performance Analysis 8.3 Caches 8.4 Virtual Memory 8.5 \* Memory-Mapped I/O 8.6 \* Real World Perspective: IA-32 Memory and I/O Systems 8.7 Summary Appendix A Digital System Implementation A.1 Introduction A.2 74xx Logic A.3 Programmable Logic A.4 Application-Specific Integrated Circuits A.5 Data Sheets A.6 Logic Families A.7 Packaging and Assembly A.8 Transmission Lines A.9 Economics Appendix B MIPS Instructions No. of pages: 592 Language: English Copyright: © Morgan Kaufmann 2007 Published: March 2, 2007 Imprint: Morgan Kaufmann Book ISBN: 9780080547060 David Harris is the Harvey S. Before attending Stanford, she received a B.S. in Electrical and Computer Engineering from Brigham Young University. Her recent exploits include researching sketching interfaces for digital circuit design, acting as a science correspondent for a National Public Radio affiliate, and learning how to kite surf. Architecture Chapter 7. Since then, he has consulted at Sun Microsystems, Hewlett-Packard, Broadcom, and other design companies. Harris is an associate professor of engineering at Harvey Mudd College. Combinational Logic Design Chapter 3. Sarah has also worked at Hewlett-Packard, the San Diego Supercomputer Center, and Nvidia. Sarah loves teaching and experimenting in the lab. Assembly language programming unveils the inner language spoken by the processor. The inner workings of a processor seem almost magical to the uninitiated, yet prove to be straightforward when carefully explained. There are dozens of good books in print on digital design. She speaks four languages and looks forward to learning more in the near future. David Harris is the Harvey S. Digital Design and Computer Architecture begins with a modern approach by rigorously covering the fundamentals of digital logic design and then introducing Hardware Description Languages (HDLs). Digital Building Blocks Chapter 6. Memory and I/O Systems Appendix A. There are also several good books about computer architecture, especially the classic texts of Patterson and Hennessy. The course can be taught without prerequisites. About the Authors. David Money Harris is a professor of engineering at Harvey Mudd College. When he is not at work, he can usually be found hiking, mountaineering, or rock climbing. Harris is an Assistant Professor of Engineering at Harvey Mudd College. Digital System Implementation Appendix B. She speaks four languages and looks forward to learning more in the near future. Assistant Professor of Engineering, Harvey Mudd College, Claremont, CA, USA Write a review There are currently no reviews for "Digital Design and Computer Architecture" Loading Preview Sorry, preview is currently unavailable.

Computer-aided design (CAD) is the use of computers (or workstations) to aid in the creation, modification, analysis, or optimization of a design. This software is used to increase the productivity of the designer, improve the quality of design, improve communications through documentation, and to create a database for manufacturing. Designs made through CAD ... In computing, a word is the natural unit of data used by a particular processor design. A word is a fixed-sized datum handled as a unit by the instruction set or the hardware of the processor. The number of bits or digits in a word (the word size, word width, or word length) is an important characteristic of any specific processor design or computer architecture.

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